

Professor Tapio Saramäki  
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**Tapio Saramäki** was born in Orivesi, Finland, on June 12, 1953. He has received the Diploma Engineer (with honors) and Doctor of Technology (with honors) degrees in electrical engineering from the Tampere University of Technology (TUT), Tampere, Finland, in 1978 and 1981, respectively.

Since 1977, he has held various research and teaching positions at TUT, where he is currently a Professor of signal processing and a Docent of telecommunications (a scientist having valuable knowledge for both the research and education at the corresponding laboratory). He is also a Co-founder and a System-Level Designer of VLSI Solution Oy, Tampere, Finland, originally specializing in VLSI implementations of Sigma-Delta modulators and analog and digital signal processing algorithms for various applications. He is also the President of Aragit Oy Ltd., Tampere, Finland, which was founded by four TUT professors, specializing on various services for the industry, including the application of information technology to numerous applications. In 1982, 1985, 1986, 1990, and 1998 he was a Visiting Research Fellow (Professor) with the University of California, Santa Barbara, in 1987 with the California Institute of Technology, Pasadena, and in 2001 with the National University of Singapore. His research interests are in digital signal processing, especially filter and filter bank design, VLSI implementations, and communications applications, as well as approximation and optimization theories. He has written almost 300 international journal and conference articles, various international book chapters, and holds three world-wide used patents on the efficient implementations of FIR filters as VLSI circuits and one patent for designing and implementing over-sampled generalized DFT filter banks.

Dr. Saramäki received the 1987 Guillemin–Cauer Award for the best paper of the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS as well as two other best paper awards. He was elected in 2002 as the Fellow of IEEE for “contributions for the design and implementation of digital filters and filter banks”, and in 2004 as the Fellow (Honorary Member) of the Russian A. S. Popov Society for Radio-Engineering, Electronics, and Communications (the highest membership grade in the society and the 80th Fellow since 1945) for “great contributions to the development of DSP theory and methods and great contributions to the consolidation of relationships between Russian and Finnish organizations”. He is also a founding member of the Median-Free Group International. He was an Associate Editor of the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS–II: ANALOG AND DIGITAL SIGNAL PROCESSING from 2000 to 2001, and is currently an Associate Editor of *Circuits, System, and Signal Processing* (from 2003 to 2008). He was also a Distinguished Lecturer of the IEEE Circuits and Systems Society (from 2002 to 2003) and the Chairman of the IEEE Circuits and Systems DSP Technical Committee (from May 2002 to May 2004). Furthermore, he has been very active in taking part in various duties for organizing several international conferences and workshops including, among others, ISCAS, EUSIPCO, ECCTD, IASTED, and ISPA, as well as annual TICSP (Tampere International Center for Signal Processing) workshops concentrating on transforms, multi-rate filter banks, spectral methods, and multi-rate signal processing in general.